

IN THE DRAWINGS:

The attached sheets of drawings include changes to Figures 5A, 10A, and 10B. The first attached sheet including Figures 5A and 5B replaces the original sheet including Figures 5A and 5B. "Application Memory Space 225" in Figure 5A has been changed to "Application Memory Space 227" to match the description in paragraph [0061] of the specification. The second attached sheet including Figures 10A and 10B replaces the original sheet including Figures 10A and 10B. Figures 10A and 10B have each been amended to include text labels.

Attachment: Replacement Sheets

REMARKS

This is intended as a full and complete response to the Office Action dated June 25, 2007, having a shortened statutory period for response set to expire on September 25, 2007. Claims 1-20 are pending in the application. By way of this reply, Applicants are amending claims 1, 9, and 16, canceling claims 6, 7, 13, 14, and 18, and adding claims 21-25.

Claims 1-20 stand rejected under 35 U.S.C. § 102(e) as anticipated by Hayes (U.S. 2003/0158906).

Claim 1, as amended, recites the steps of uploading the first processed frame data to a user buffer in a first portion of memory that is allocated to an application program as well as uploading the second processed frame data to a legacy buffer in a second portion of the memory that is allocated to a software driver configured to communicate between the offload unit and a TCP stack. Hayes fails to teach or disclose these features. In Figure 7 Hayes shows memory 108, but does not teach or suggest that frame data is stored in the memory. In particular, Hayes fails to teach or suggest that portions of the memory are allocated to a software driver and an application program. Therefore, claim 1 and its dependent claims, claims 2-6, 8 and new claim 21, are patentable over Hayes.

Claim 9, as amended, recites a memory configured to store user buffers in a first portion that is allocated to an application program and to store legacy buffers in a second portion that is allocated to the software driver and an offload unit that is configured to (i) upload partially processed frames to at least one of the legacy buffers and (ii) upload the payload data to at least one of the user buffers. Again, as set forth above, Hayes fails to teach or disclose these features. Therefore, claim 9 and its dependent claims, claims 10-12, 15 and new claims 22-25, are patentable over Hayes.

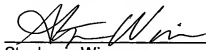
Claim 16, as amended, recites the steps of uploading processed frame data to a user buffer in a first portion of memory that is allocated to an application program and uploading frame data for non-delegated TCP connections to a legacy buffer in a second

portion of the memory that is allocated to a software driver configured to communicate between the offload unit and the TCP stack. Again, as set forth above, Hayes fails to teach or disclose these features. Therefore, claim 16 and its dependent claims, claims 17, 19-20, are patentable over Hayes.

New claim 21 recites the step of issuing an interrupt to the CPU after the second processed frame data is uploaded to the legacy buffer. Interrupting the CPU when a legacy buffer is uploaded is described in paragraph [0084] of the present application. None of the cited references teach or suggest this feature. Therefore, claim 21 is patentable over the cited references. New claims 22-25 recite the limitations of user buffers and legacy buffers that are stored in physically contiguous memory locations or physically non-contiguous memory locations. User buffer layout in the application memory space is shown in Figure 5A and described in paragraph [0061] of the present application. Legacy buffer layout in the driver memory space is shown in Figure 5C and described in paragraph [0065] of the present application. None of the cited references teach or suggest this feature. Therefore, claim 21 is patentable over the cited references.

Having addressed all issues set out in the office action, Applicant respectfully submits that the claims are in condition for allowance and respectfully request that the claims be allowed.

Respectfully submitted,



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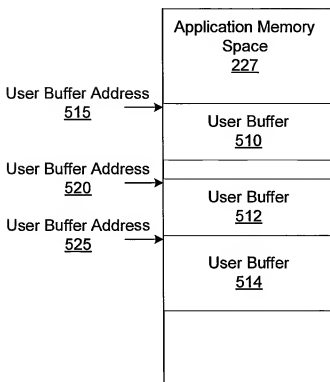


Fig. 5A

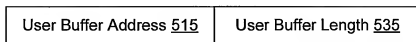


Fig. 5B

Field <u>1007</u>	Field <u>1005</u>	Field <u>1010</u>
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Fig. 10A

Field <u>1007</u>	Field <u>1015</u>	Field <u>1020</u>	Field <u>1025</u>
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Field <u>1007</u>	Field <u>1015</u>	Field <u>1030</u>	Field <u>1035</u>
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Fig. 10B